

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,058,778 B2
APPLICATION NO. : 09/943320
DATED : June 6, 2006
INVENTOR(S) : Travis Swanson

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- Col. 4 Claim 1, line 17, before "said method" insert --wherein at least one of said pins has selectable functionality--;
- line 21, after "pins" insert --having said selectable functionality--;
- Col. 4 Claim 4, line 28, change "2" to --1--;
- Col. 4 Claim 8, line 41, replace "control" with --chip select--;
- line 43, replace "control" with --chip select--;
- Col. 4 Claim 12, line 53, replace "at least one" with --an--;
- line 56, replace "a control signal" with --a clock signal--;
- line 60, insert before "signal" --control--;
- line 61, replace "control" with --clock--;
- Col. 5 Claim 16, line 4, replace "at least one" with --an--;
- lines 6-8, replace "selectable functionality in accordance with a type of memory, said functionality including control chip select, and clock function" with --a function selected in accordance with a type of memory, said circuitry operative to select one of at least a chip select function and a clock function--;
- Col. 5 Claim 17, lines 9-11, replace "said functionality of said output pin is one of a chip select signal and a clock signal" with --selection of the chip select function provides a chip select signal to said output pin; and wherein selection of the clock function provides a clock signal to said output pin--;
- Col. 5 Claim 18, lines 18-19, replace "functionality including control, chip select, and clock functions" with --memory controller operative to select a chip select function and a clock function for each of said at least one of said pins having the selectable functionality--;
- Col. 5 Claim 19, lines 20-21, replace "said selectable functionality comprises" with --selection of the clock function provides--;
- Col. 5 Claim 20, lines 22-23, replace "said selectable functionality comprises" with --selection of the chip select function provides--;
- Col. 5 Claim 21, lines 32-35, replace "a subset of said pins has selectable functionality, said functionality based on said type of said memory modules, said functionality including control, chip select, and clock functions" with --each one of a subset of said pins has selectable functionality, said selectable functionality based on said type of said memory modules, said selectable functionality including a chip select function and a clock function--;
- Col. 5 Claim 24, lines 47-48, replace "said type of memory, said functionality including control, chip select, and clock functions" with --said type of memory, each of said at least one pin configurable by said means for configuring having selectable functionality including a chip select function and a clock function--;

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- Col. 5 Claim 25, line 54, replace "control" with --chip select--;
- Col. 6 Claim 26, line 6, replace "at least one" with --an--;
- line 9, insert --for selecting-- after "means";
- line 13, insert --one of-- after "selecting"; delete "control"
- line 14, insert --for selecting and said clock signal means-- after "means";
- line 16, replace "at least one" with --an--;
- line 20, replace "control" with --chip select--;
- line 23, replace "control" with --chip select--;
- Col. 6 Claim 28, line 26, replace "at least one" with --an--;
- lines 28-30, replace "said functionality including control, chip select, and clock functions" with --said selectable functionality including a chip select function and a clock function--;
- line 35, replace "at lease some" with --one--;
- line 37, insert --selectable-- after "said";
- lines 38-39, replace "said functionality including control, chip select, and clock functions" with --said selectable functionality including a chip select function and a clock function--;
- Col. 6 Claim 30, line 48, insert --each one of-- before "a subset of...";
- lines 49-51, replace "said functionality based on said type of said memory modules and including control, chip select, and clock functions" with --said selectable functionality based on said type of said memory modules and including a chip select function and a clock function--

Signed and Sealed this

Fifth Day of August, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office